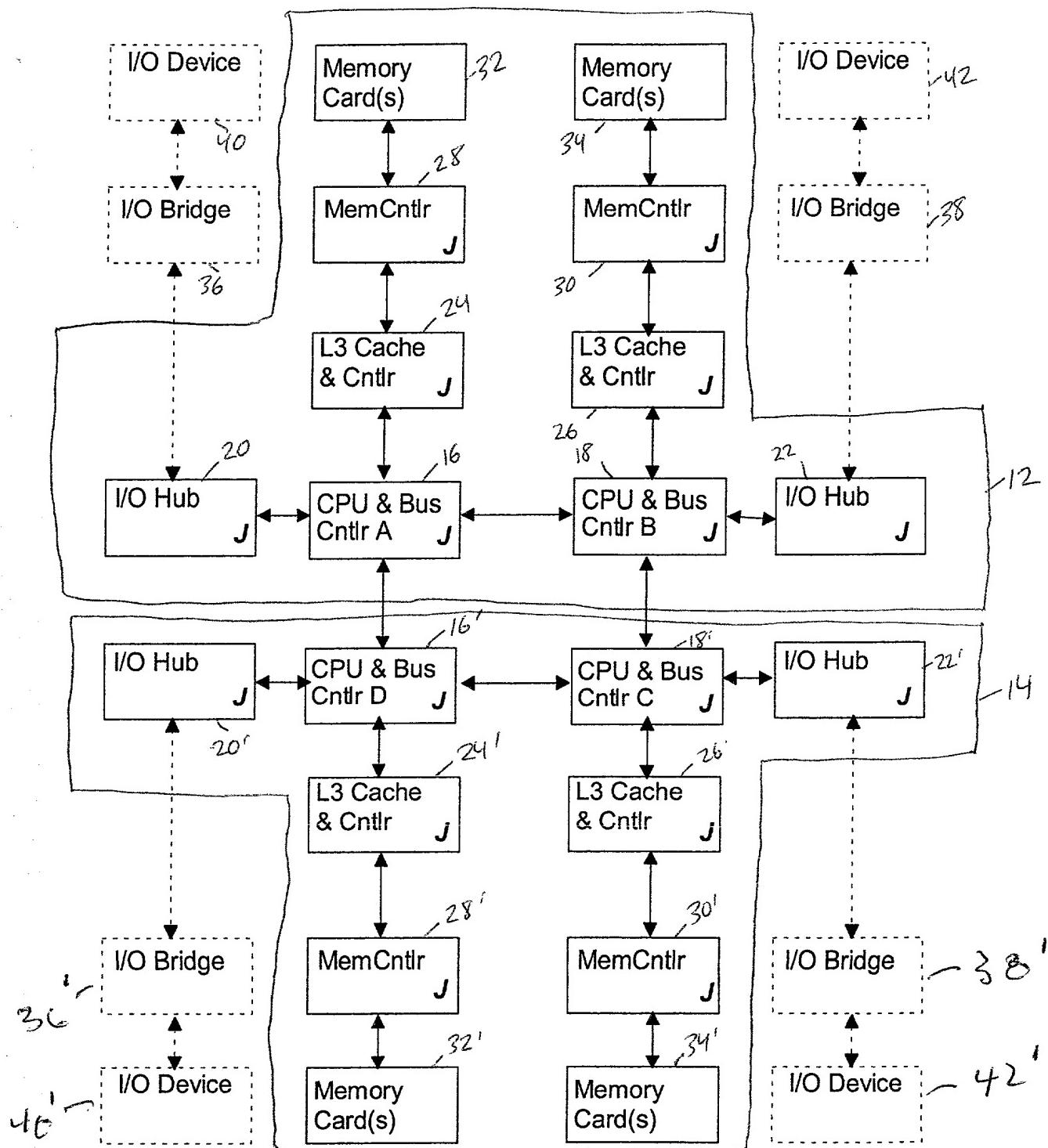


AVS 920010223 US

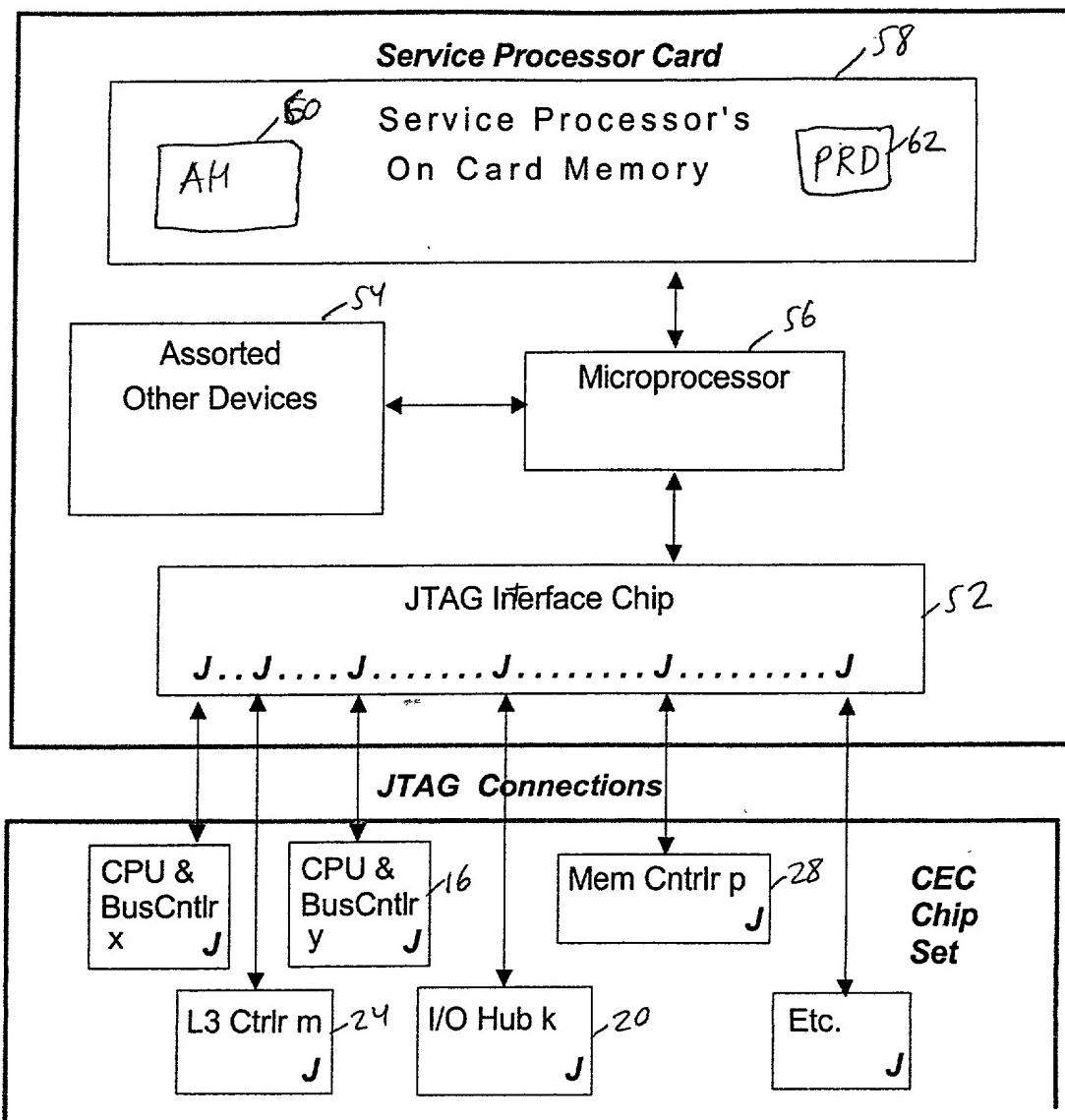
**J = chip has serial jtag connection to Service Processor HW****[] dotted box indicates remote chip or device inaccessible to SP via jtag****↔ dotted line indicates connections to and between remote chips & devices**

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Figure 1

817
aw5920610223w1

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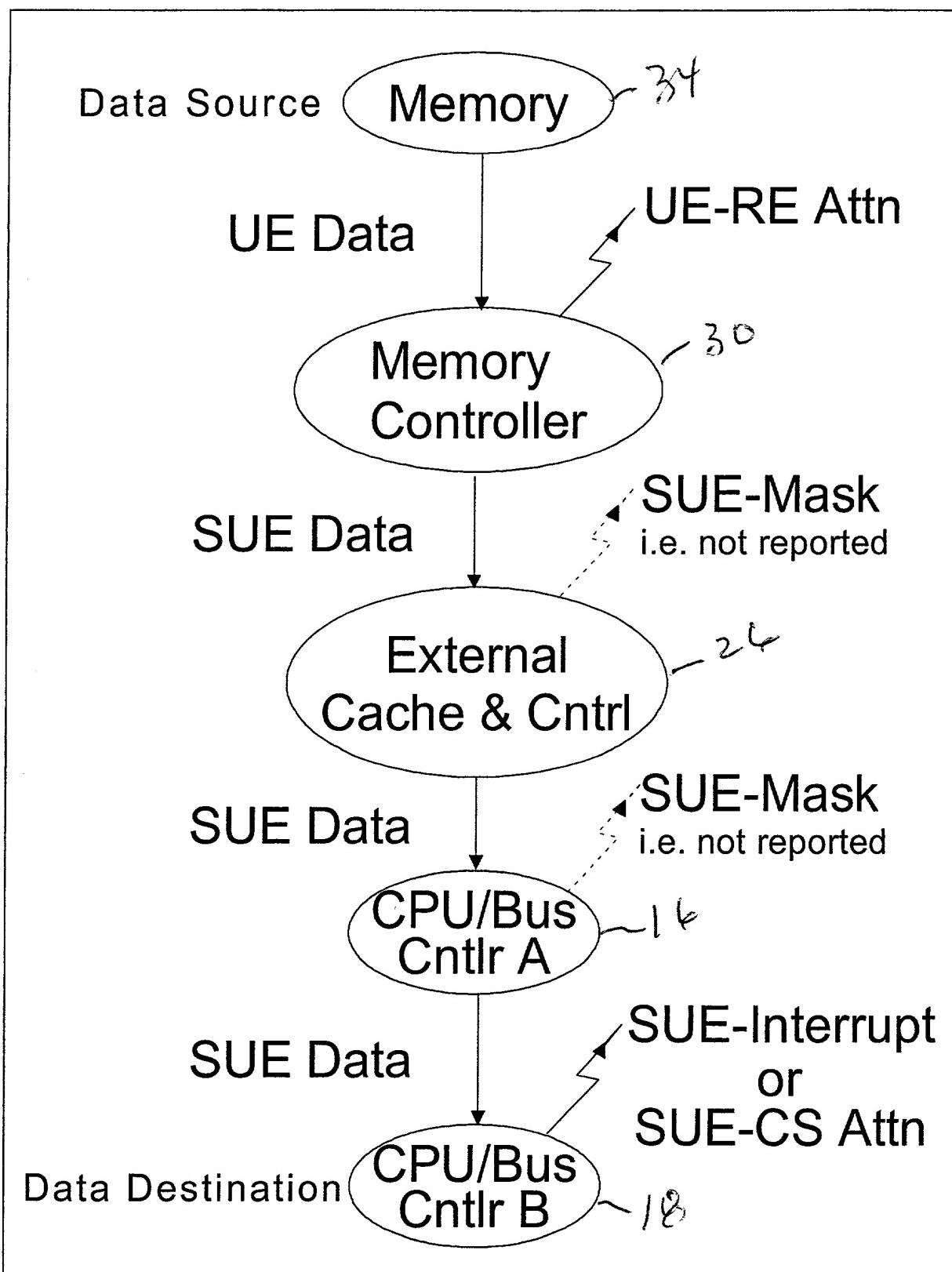


J = JTAG serial interface between Service Processor & chips in CEC

Figure 2

3/7
aus920010223 us)

Figure 3



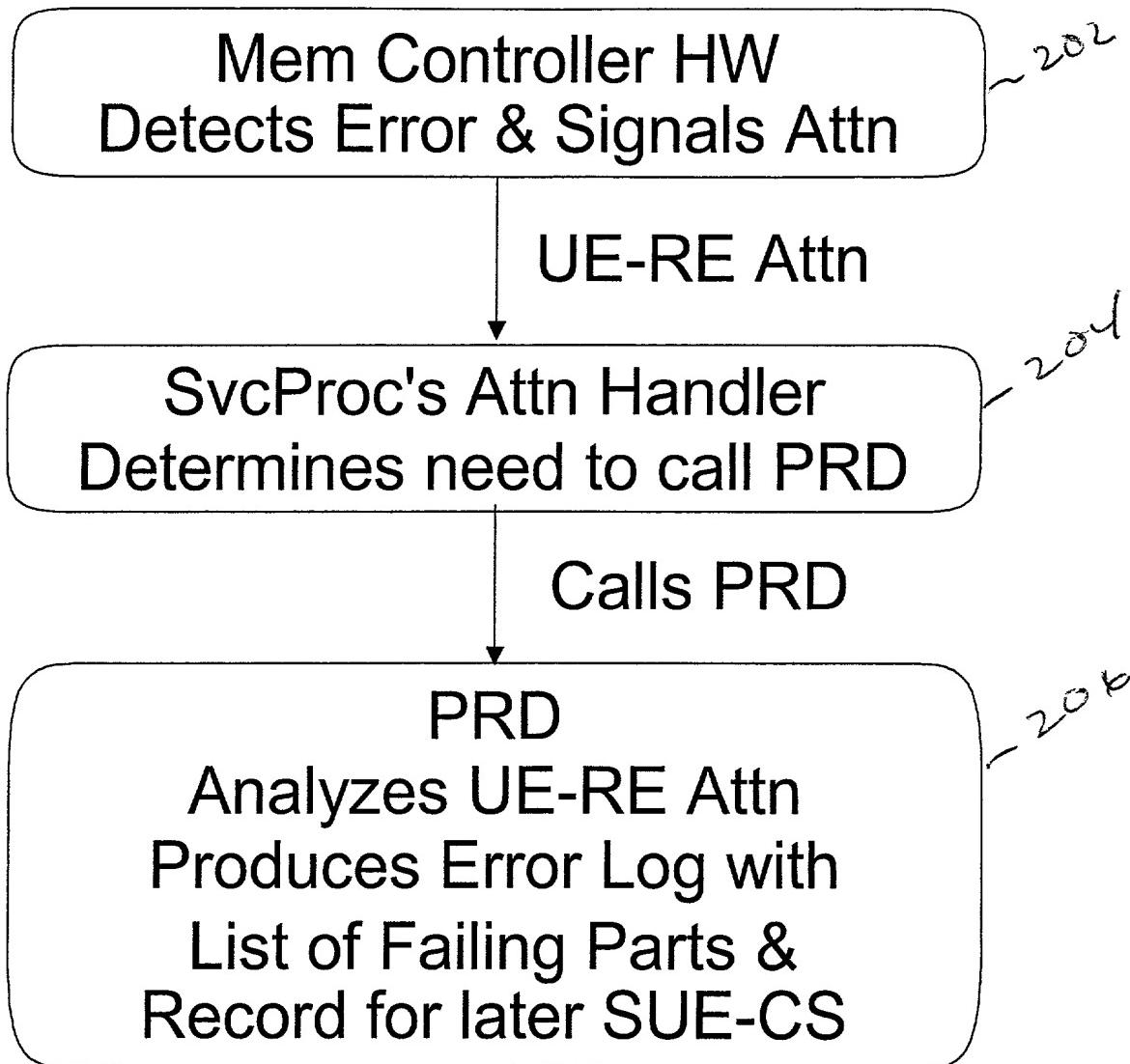


Figure 4

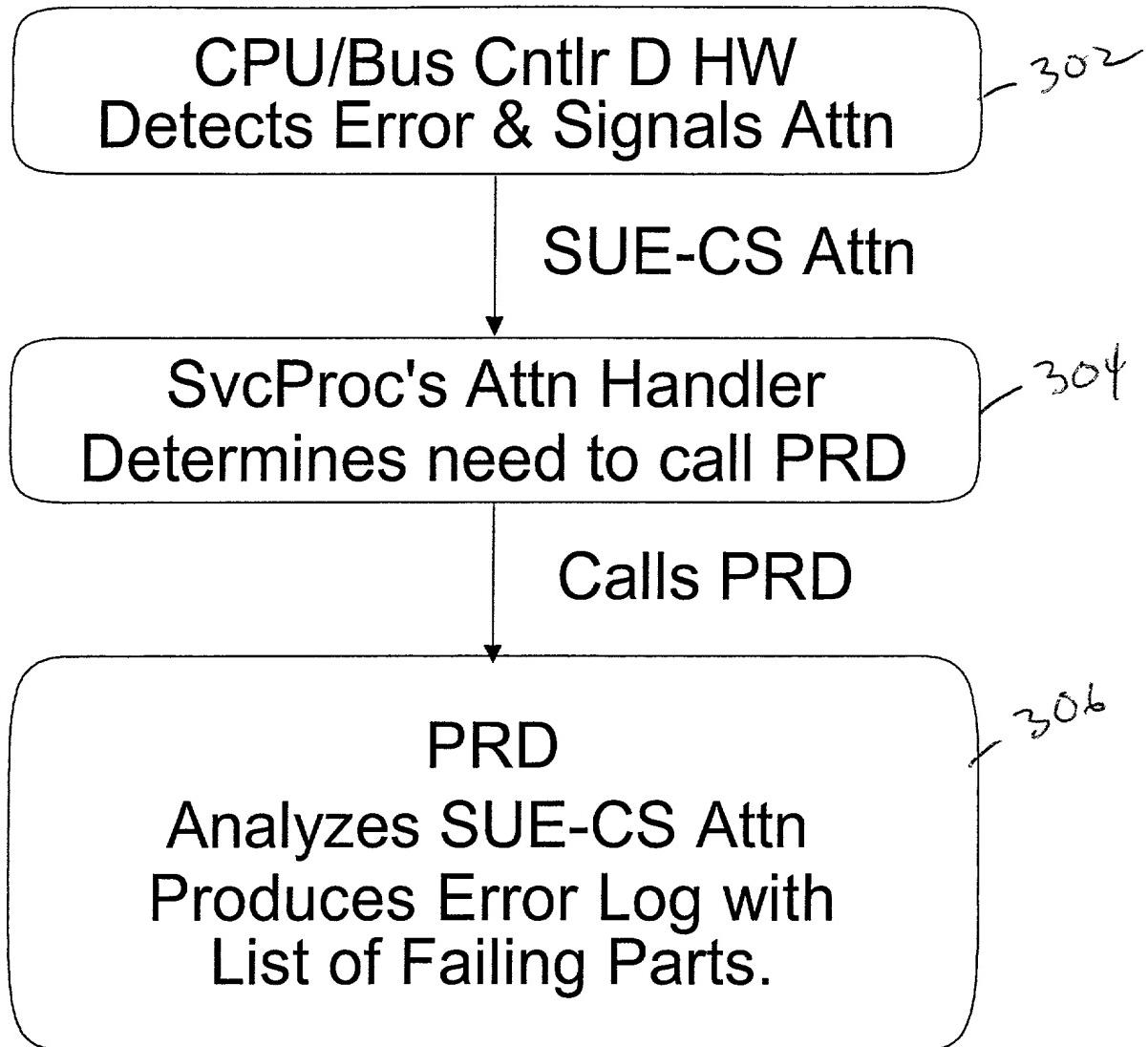


Figure 5

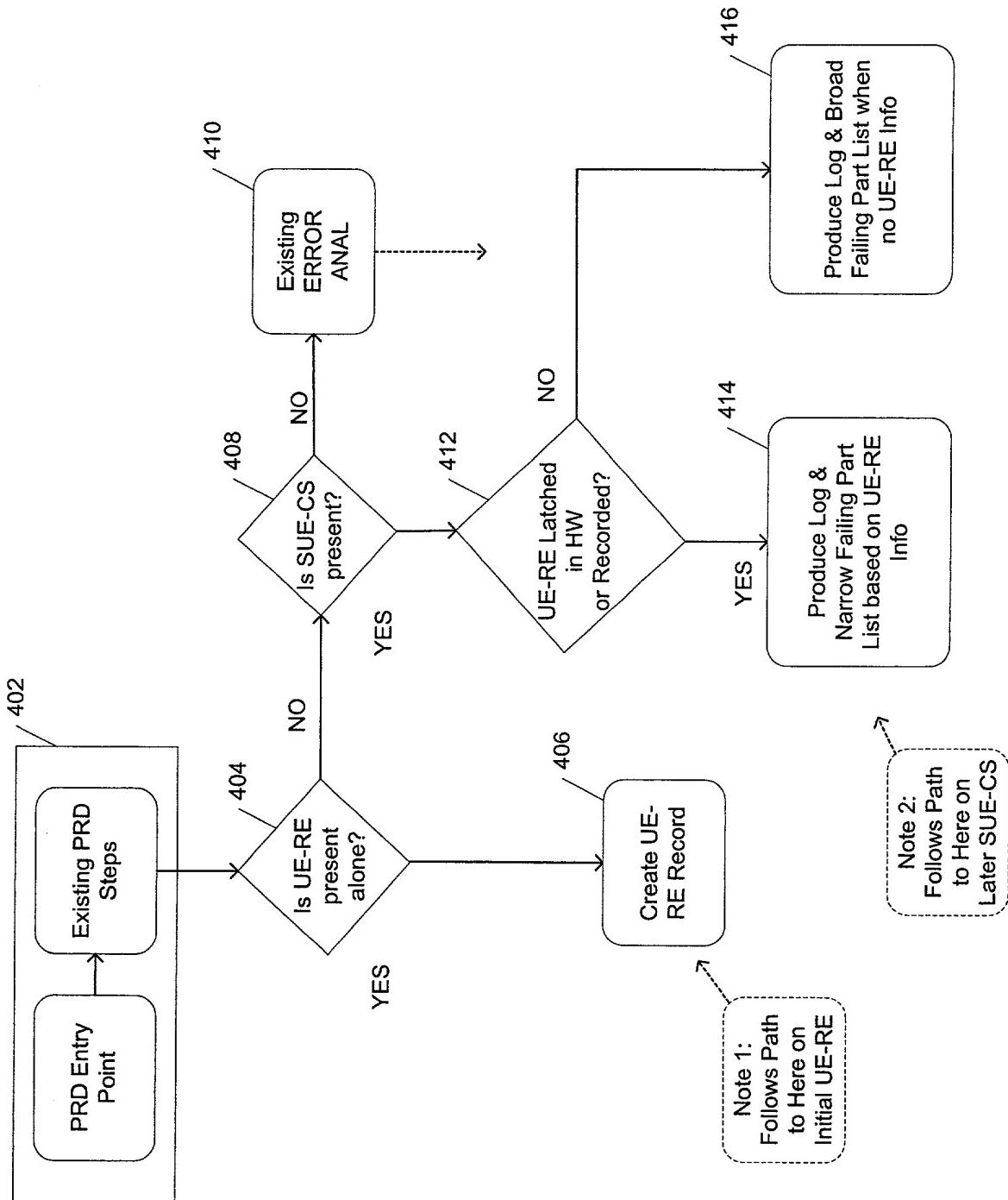


Figure 6

Figure 7

